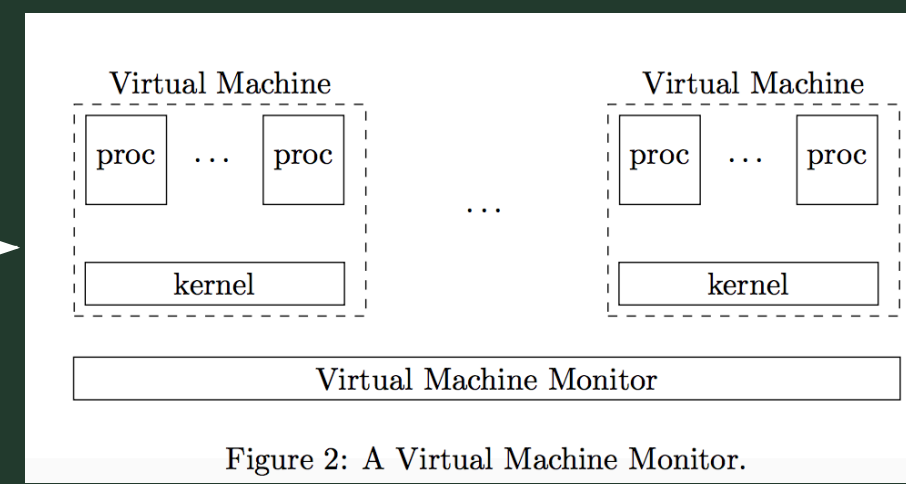
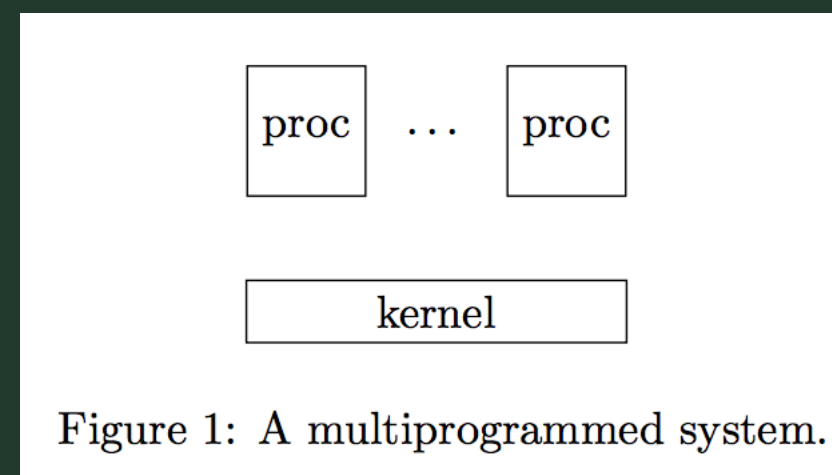


## Hardware Assisted Virtualization

In the hardware-assisted virtualization technique we try to execute the instructions of the target machine directly on the host processor, as much as possible.

### The Virtual Machine Monitor

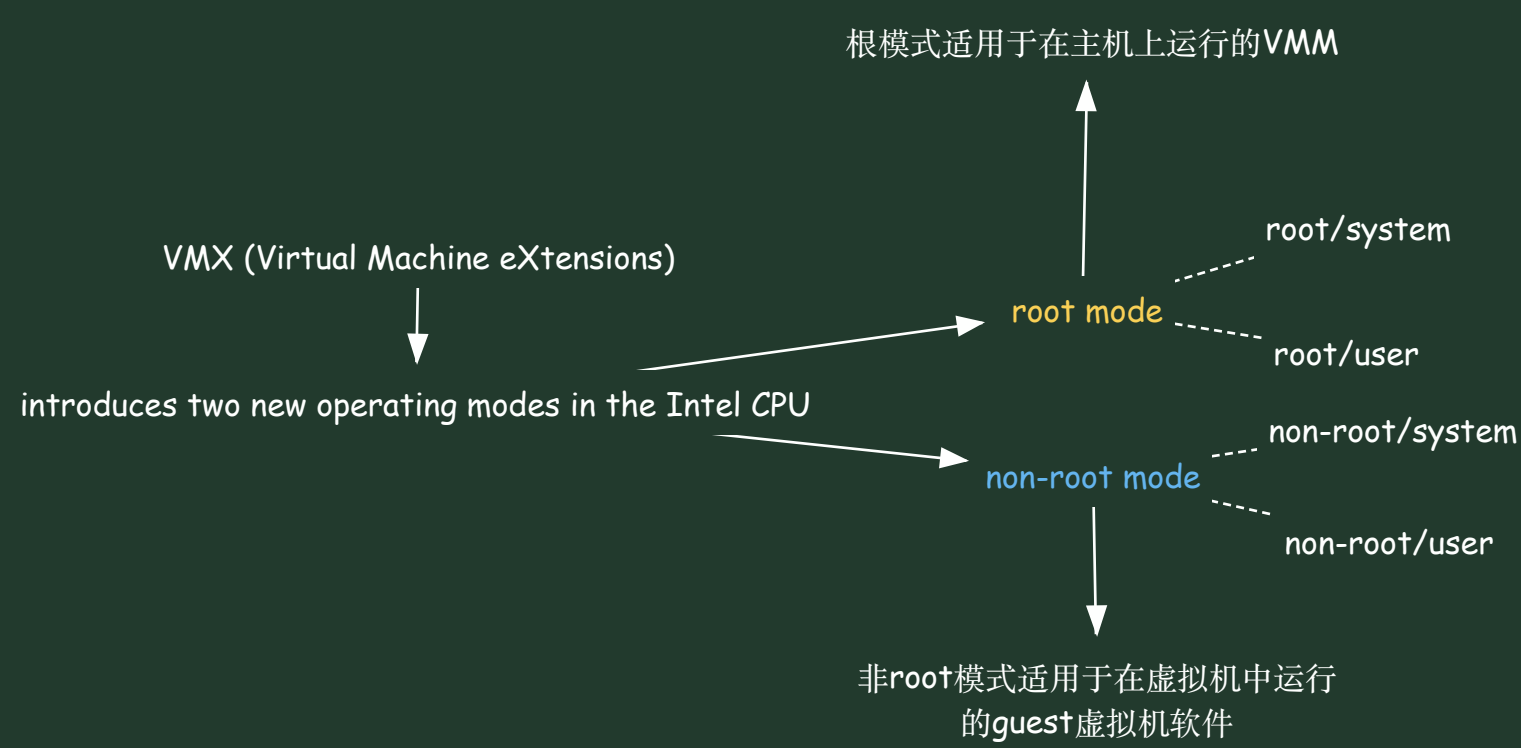


Emulating the target CPU ----- When to trap? any instruction that affects the VMM or the other VMs cannot be directly executed

Emulating the target Physical Memory

Emulating the target I/O devices ----- Emulating interrupts

### The Intel VMX technology



The main purpose of these new modes is to put hardware-controlled limitations to the actions performed by the guest system software

Virtual Machine Control Structure (VMCS) ----- 通常每个虚拟机的每个处理器都有一个  
contains all the information needed to manage the new non-root mode

the processor has a register pointing to the current VMCS

VMPTRLD ----- load the address of a VMCS, making it current

Whenever the system code tries to execute an instruction that would either violate the isolation of the VMM, or that must be emulated via software, the hardware can trap it and switch back to the VMM.

